

**IN THE CLAIMS**

This listing of claims will replace all prior versions and listings of claims in the application.

Claims 1-24. (Canceled)

**Claim 25. (Previously presented)** A circuit board having a signal interconnect post, comprising:  
a filter receiver loop in which interfering signals can be induced and comprising an electrical filter circuit connected to the post to filter conducted interfering signals traveling through the post; and  
a suppressor receiver loop in which noise suppression signals can be induced, wherein the receiver loops are oriented such that the respective interfering and noise suppression signals are induced by a same external field and travel in opposite directions through a shared conductive path.

**Claim 26. (Previously presented)** The circuit board of claim 25, wherein the filter receiver loop comprises:  
a conductor electrically coupled to the post and the electrical filter circuit;  
a circuit board ground plane; and  
wherein the shared conductive path is a path through the conductor.

**Claim 27. (Previously presented)** The circuit board of claim 26, wherein the filter receiver loop further comprises:  
a ground via electrically coupled to the conductor and the circuit board ground plane.

**Claim 28. (Previously presented)** The circuit board of claim 26, wherein the suppressor receiver loop comprises:  
the conductor; and  
a conductive coating conformingly adhered to surfaces of the electrical filter circuit.

**Claim 29. (Previously presented)** The circuit board of claim 25, wherein the interconnect post comprises one of the group consisting of a signal connector, a power connector and a power post.

**Claim 30. (Previously presented)** The circuit board of claim 25, wherein the external field is electrostatically, magnetically or electromagnetically coupled with the receiver loops.

**Claim 31. (Previously presented)** The circuit board of claim 25, further comprising:  
a conductor comprising a surface trace, and  
wherein the electrical filter circuit comprises a surface mount capacitor.

**Claim 32. (Previously presented)** The circuit board of claim 25, wherein the electrical filter circuit comprises:

one or more lumped, distributed or dissipative elements having a frequency characteristic suitable for decoupling the conducted interfering signal from a transmitted signal.

**Claim 33. (Currently amended)** The circuit board of claim 25, wherein the circuit board further comprises a destination circuit electrically coupled to the post, and wherein the noise suppression signals substantially prevent interfering signals induced in the filter receiver loop from reaching the designation circuit.

**Claim 34. (Previously presented)** The circuit board of claim 25, wherein the interconnect post receives power from an external power source and wherein the electrical filter circuit decouples high frequency conducted interfering signals from received power signals.

**Claim 35. (Currently amended)** The circuit board of ~~claim 25, claim 28~~, wherein the conductive coating is part of a EMI shield also comprising a dielectric coating interposed between selected portions of the circuit board and the conductive coating.

**Claim 36. (Currently amended)** The circuit board of claim 25, wherein the configuration of the electrical filter circuit is ~~and the arrangement of two or more elements of the circuit board are~~ determined so as to minimize field coupling characteristics of the filter receiver loop.

**Claim 37. (Previously presented)** The circuit board of claim 31, wherein the surface mount capacitor is located immediately adjacent to the interconnect post.

**Claim 38. (Previously presented)** The circuit board of claim 27, wherein the ground via is located immediately adjacent to a ground terminal of a surface mounted capacitor.

**Claim 39. (Currently amended)** The circuit board of claim 25, claim 36, wherein the area encompassed by the filter receiver loop is determined to minimize the potential coupling of the filter receiver loop with the same external field.

**Claim 40. (Previously presented)** The circuit board of claim 25, wherein the electrical filter circuit comprises one or more capacitors selected to provide a desired capacitance while minimizing parasitic inductance in the electrical filter circuit.

**Claim 41 (Previously presented)** The circuit board of claim 25, wherein the filter receiver loop and the noise suppressor loop are constructed and arranged such that an external field electrostatically, magnetically and/or electromagnetically coupled to the filter receiver loop will also be similarly coupled to the noise suppressor loop.

**Claim 42. (Previously presented)** A decoupling circuit on a printed circuit board, comprising:

a first receiver loop comprising an electrical filter circuit electrically connected via a conductive path to an interconnect post and a ground via coupled to a ground plane, the filter configured to shunt to the ground plane conducted interfering signals on the interconnect post; and

a second receiver loop, comprising a conductive coating adhered to surfaces of the decoupling circuit, and the conductive path, adapted to have induced therein a signal having a direction and magnitude sufficient to cancel at least a portion of interfering signals induced in the first receiver loop.

**Claim 43. (Previously presented)** The decoupling circuit of claim 42, wherein the conductive coating is conformally secured to exterior surfaces of the electrical filter circuit and insulated from the electrical filter, interconnect post and conductive path.

**Claim 44. (Currently amended)** The decoupling circuit of claim 42, ~~claim 41~~, wherein the electrical filter circuit comprises one or more lumped, distributed or dissipative elements having a frequency characteristic providing a predetermined signal filtering capability.

**Claim 45. (Previously presented)** The decoupling circuit of claim 42, wherein the first and second receiver loops are constructed and arranged to minimize respective field coupling characteristics.

**Claim 46. (Previously presented)** The decoupling circuit of claim 42, wherein the electrical filter circuit comprises:

a surface-mounted capacitor mounted immediately adjacent to the interconnect post and connected electrically between the interconnect post and the group via, wherein the ground via is located immediately adjacent to the surface-mounted capacitor.

**Claim 47 (Previously presented)** The decoupling circuit of claim 42, further comprising:

a plurality of ground lands located immediately adjacent to and radially-spaced around electrical filter components, wherein the conductive coating is electrically connected to the ground lands, and wherein the conductive coating, ground plane and ground lands together form a grounded compartment around the electrical filter.

**Claim 48 (Previously presented)** The decoupling circuit of claim 42, wherein the receiver loops are oriented such that the respective interfering signals are induced by a same external field and travel in opposite directions through the conductive path.

**Claim 49. (Previously presented)** The decoupling circuit of claim 42, wherein the interconnect post comprises one of the group consisting of a signal connector, a power connector and a power post.

**Claim 50 (Cancelled)**

Claim 51. (Previously presented) The decoupling circuit of claim 48, wherein the external field is electrostatically, magnetically or electromagnetically coupled with the receiver loops.

Claim 52. (Currently amended) The decoupling circuit of claim 42, wherein the ~~conductor~~ conductive path comprises a surface trace, and wherein the electrical filter circuit comprises a surface mount capacitor.

Claim 53. (Previously presented) The decoupling circuit of claim 48, wherein the area encompassed by the receiver loops is determined to minimize the potential coupling of the receiver loops with the external field.

Claim 54. (Previously presented) The decoupling circuit of claim 42, wherein the electrical filter circuit comprises one or more capacitors selected to provide a desired capacitance while minimizing parasitic inductance in the electrical filter circuit.

Claim 55 (Previously presented) The decoupling circuit of claim 42, wherein the first and second receiver loops are constructed and arranged such that an external field electrostatically, magnetically and/or electromagnetically coupled to the first receiver loop will also be similarly coupled to the second receiver loop.

Claim 56 (Previously presented) A printed circuit board comprising:  
a printed wiring board;  
an interconnect post mounted on the printed wiring board;  
a ground plane within the printed wiring board;  
an electrical filter circuit mounted on the printed wiring board and electrically connected to the interconnect post;  
a plurality of ground lands radially spaced around and electrically connected to the electrical filter circuit, at least one of which is electrically connected to the filter circuit; and  
a conductive coating adhered to surfaces of the electrical filter circuit,  
wherein the electrical filter circuit shunts conducted interfering signals traveling through the interconnect post to the ground plane via the at least one ground land.

Claim 57. (Previously presented) The printed circuit board of claim 56, wherein the electrical filter circuit, the interconnect post, a first ground land, the ground plane, and interconnecting traces form a first receiver loop capable of being coupled to an external field that induces an interfering signal in at least one of the traces of the first receiver loop.

Claim 58. (Previously presented) The printed circuit board of claim 57, wherein the conductive coating, the first ground land, electrical filter components and the interconnecting traces form a second receiver loop adapted to be coupled to the external field which induces a signal in the second receiver loop that cancels the interfering signal induced in the at least one trace of the first receiver loop.

Claim 59. **(Previously presented)** The printed circuit board of claim 56, wherein the interconnect post comprises one of the group consisting of a signal connector, a power connector and a power post.

Claim 60. **(Previously presented)** The printed circuit board of claim 58, wherein the external field is electrostatically, magnetically or electromagnetically coupled with the receiver loops.

Claim 61. **(Previously presented)** The printed circuit board of claim 58, wherein the at least one trace comprises a surface trace, and wherein the electrical filter circuit comprises a surface mount capacitor.

Claim 62. **(Previously presented)** The printed circuit board of claim 56, wherein the electrical filter circuit comprises:  
one or more lumped, distributed or dissipative elements having a frequency characteristic suitable for decoupling the conducted interfering signal from a transmitted signal.